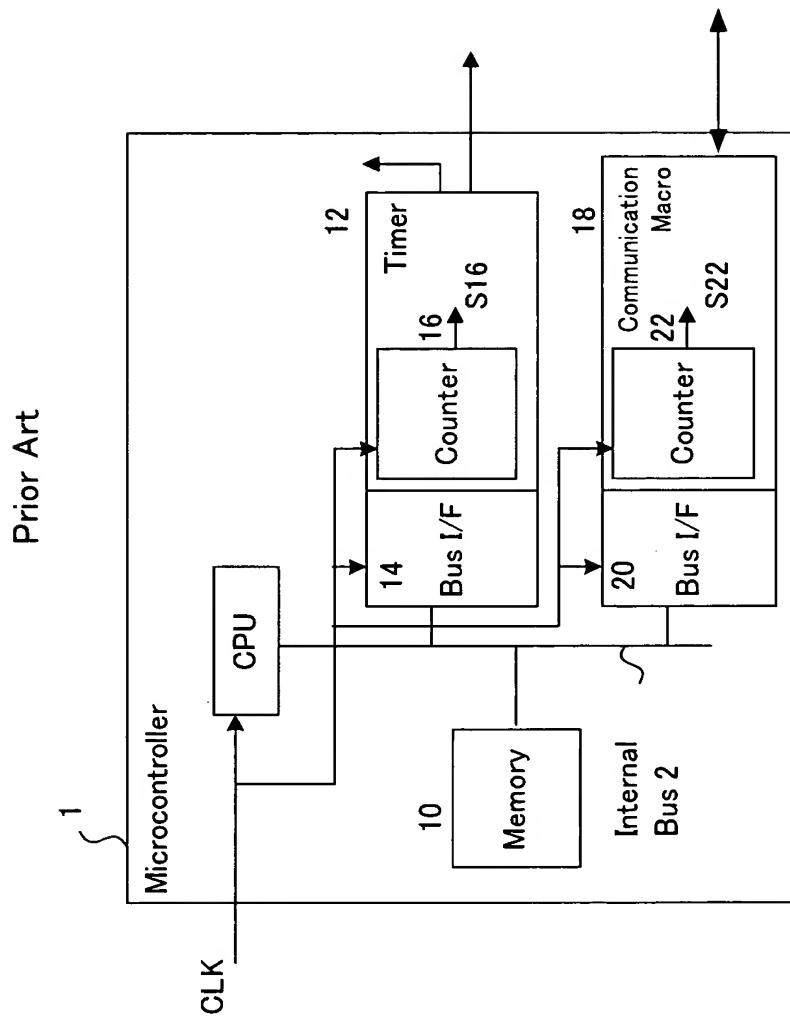
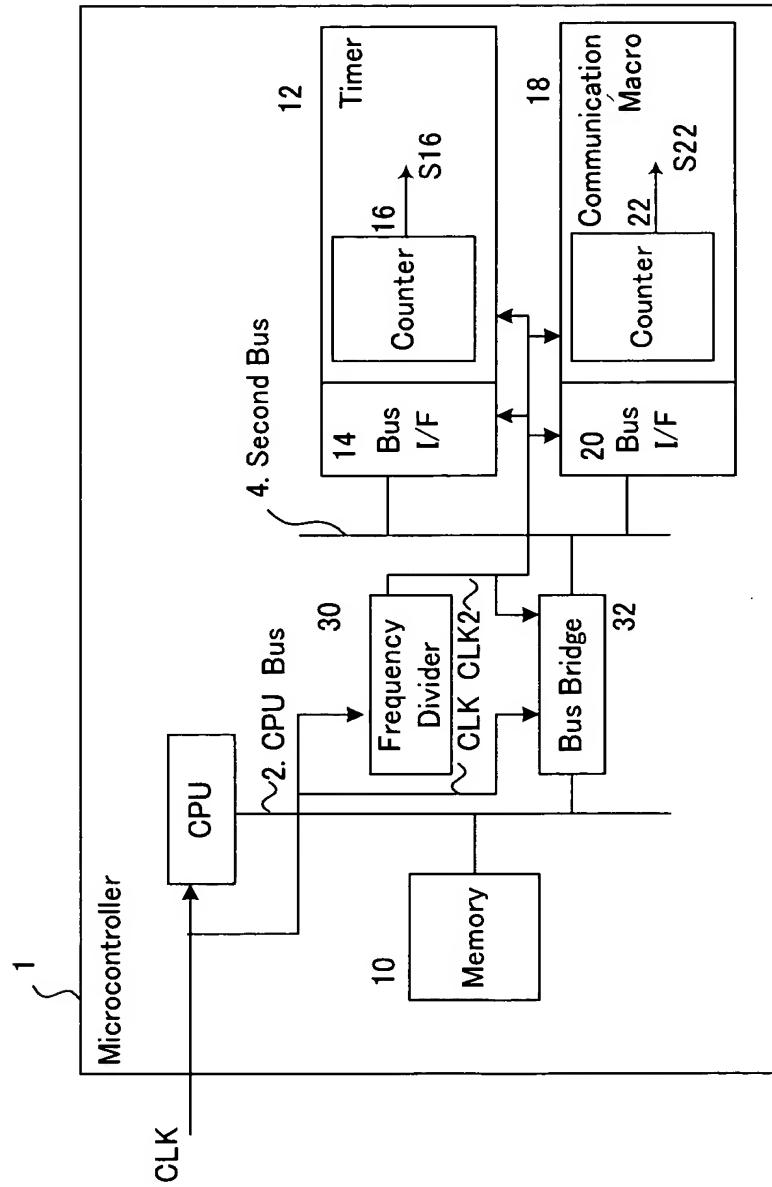


**FIG. 1**



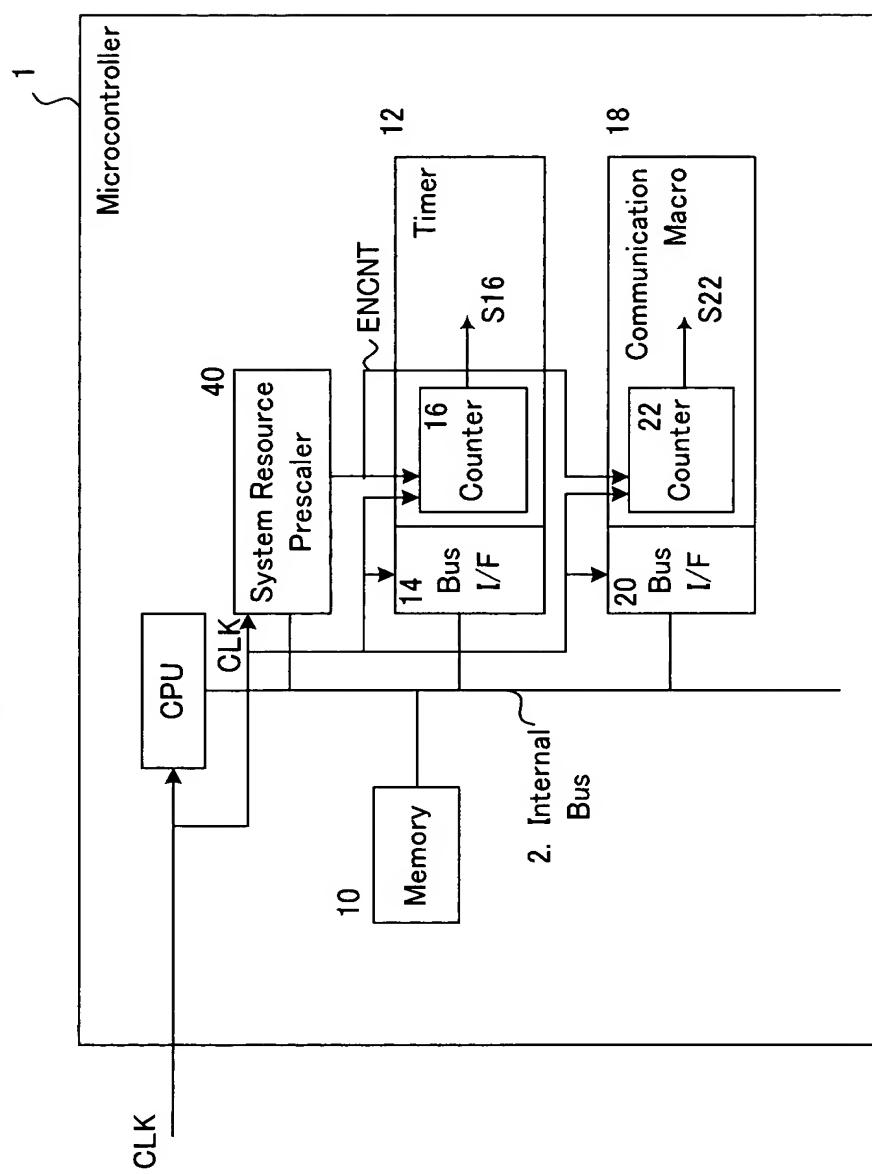
**FIG. 2**

Prior Art



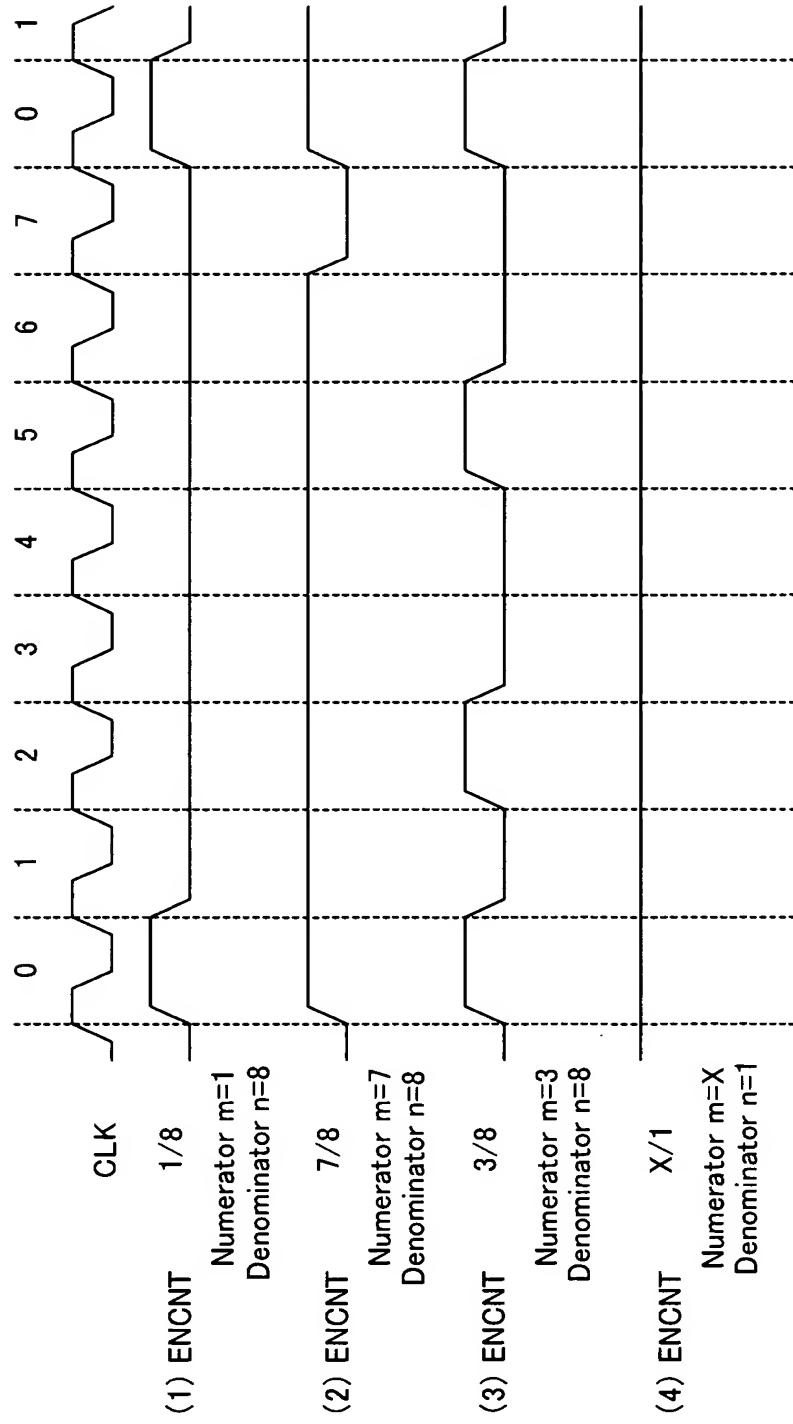
**FIG. 3**

**Microcontroller of Embodiment**

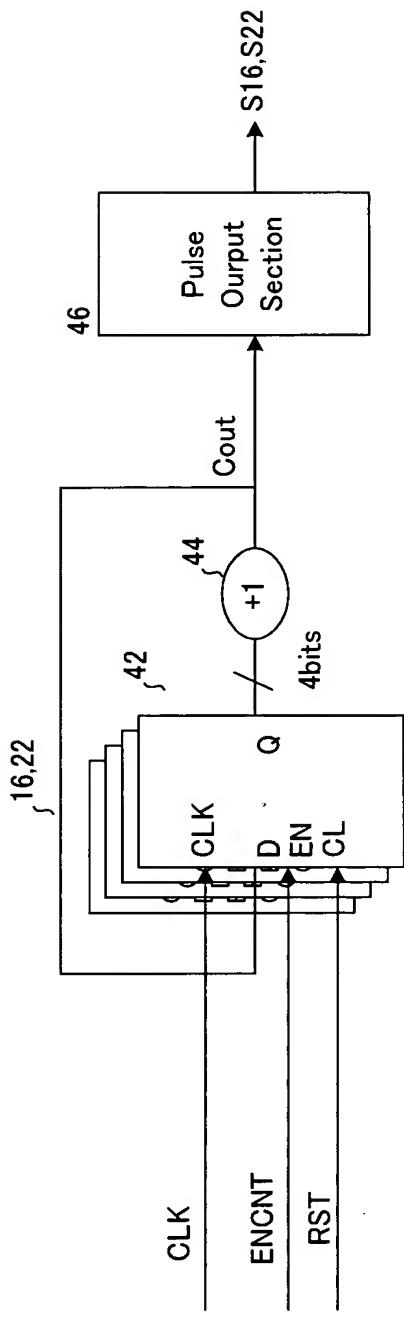


**FIG. 4**

Example of ENCNT



**FIG. 5A**



**FIG. 5B**

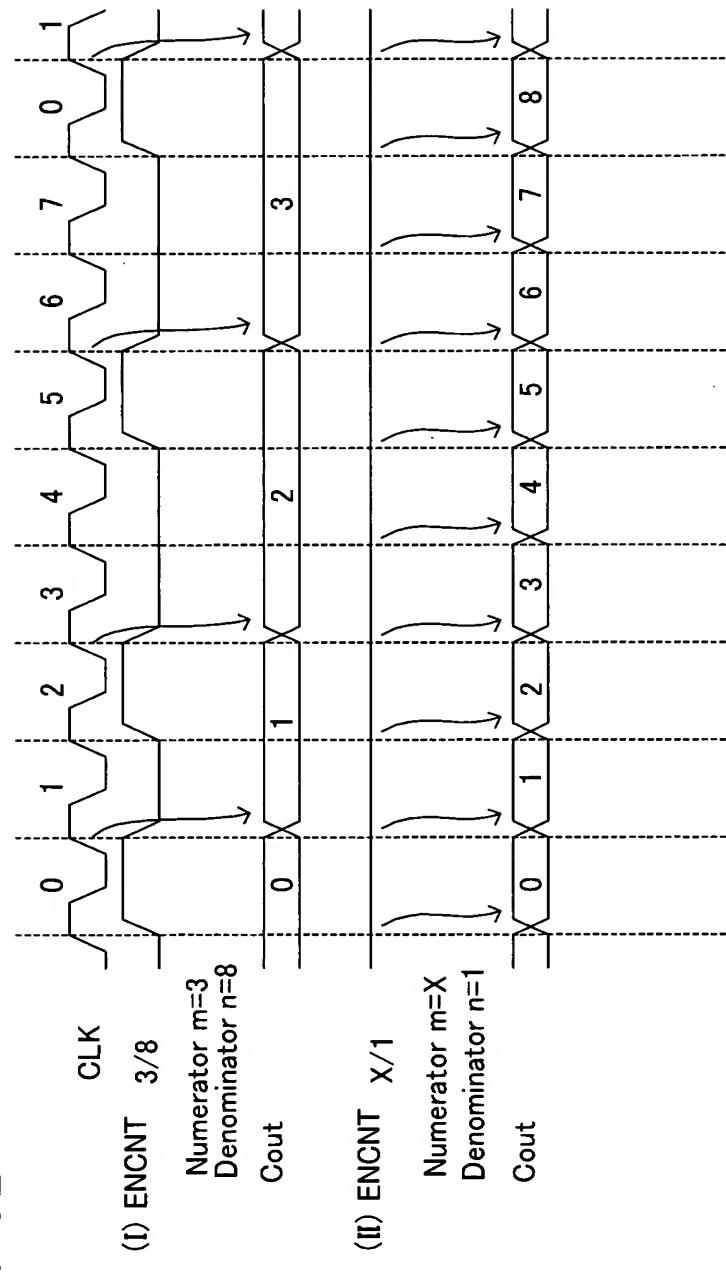
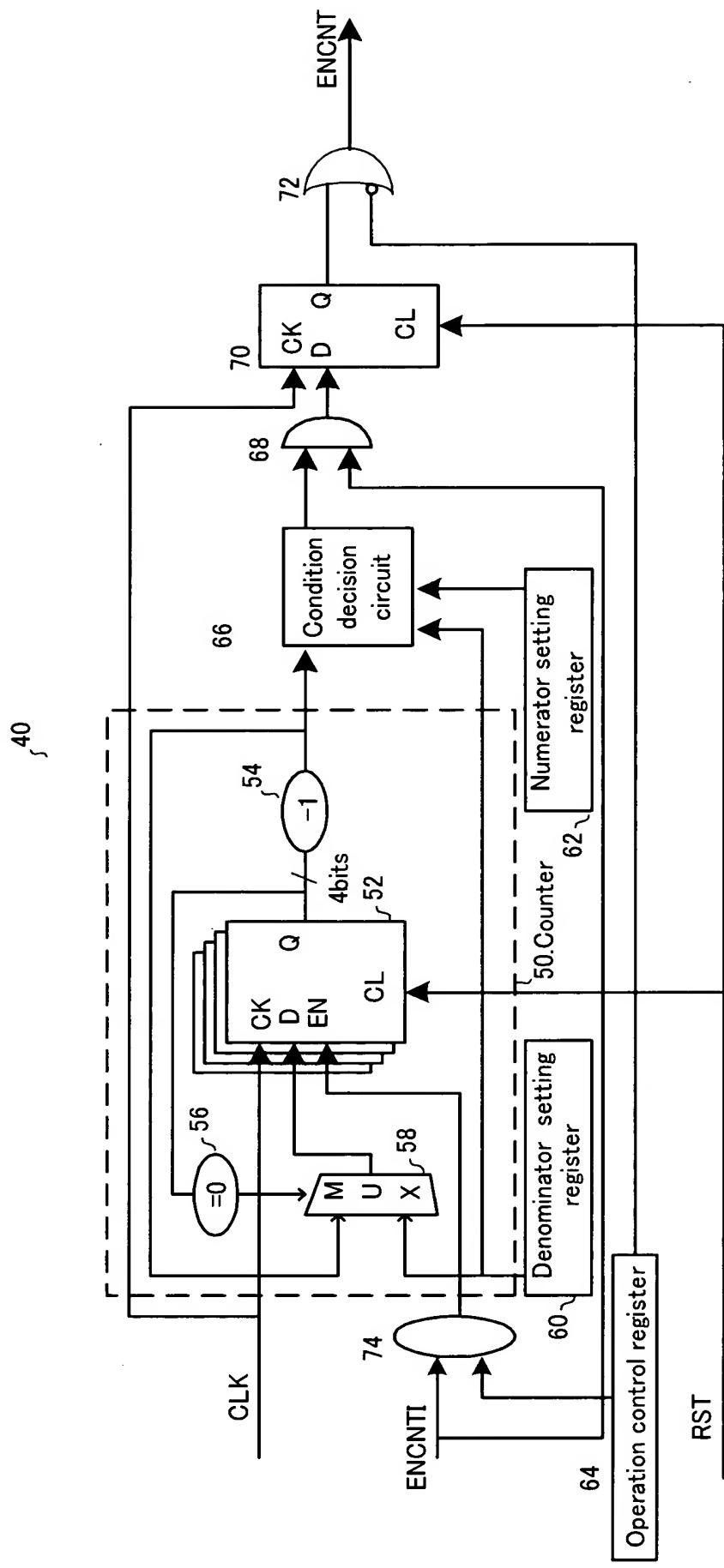


FIG. 6



**FIG. 7A**

Denominator/Numerator Setting Register 60, 62

	7	6	5	4	3	2	1	0	Initial Data				
Nume3	R/W	Nume2	R/W	Nume1	R/W	Nume0	R/W	Denom3	R/W	Denom1	R/W <th>Denom0</th> <td>R/W</td>	Denom0	R/W
Operation Control Register 64													
7	6	5	4	3	2	1	0						
-	-	-	-	-	-	-	-						
-	-	-	-	-	-	-	-						

**FIG. 7B**

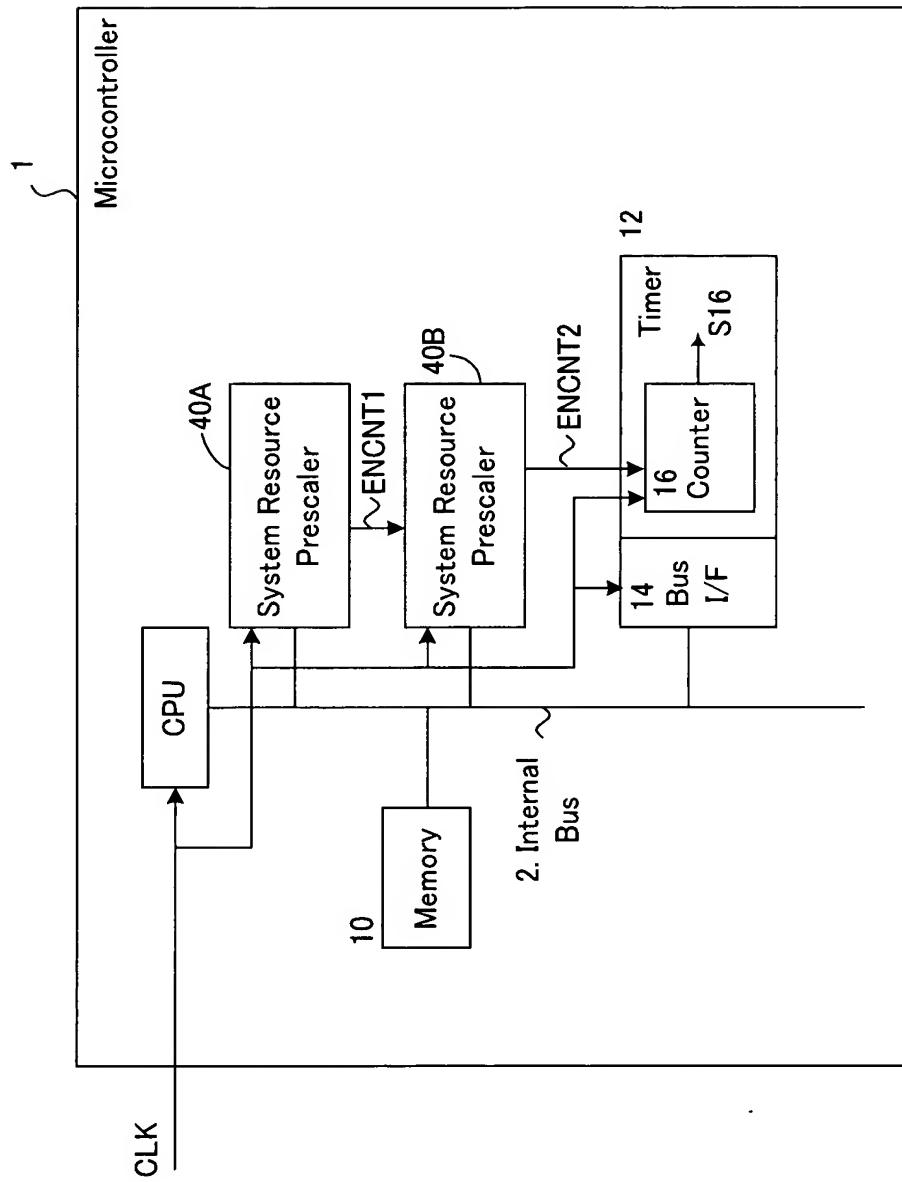
Decision Table of Condition Decision Circuit

n-1 \ m-1	0	1	2	3	4	5	6	7
0	0	0	0	0	0	0	0	0
1	-	0-1	0-1	0,2	0,2	0,3	0,3	0,4
2	-	-	0-2	0-2	0,1,3	0,2,4	0,2,5	0,2,5
3	-	-	-	0-3	0-3	0,1,3,4	0,1,3,5	0,2,4,6
4	-	-	-	-	0-4	0-4	0,1,2,4,5	0,1,2,4,6
5	-	-	-	-	-	0-5	0-5	0,1,2,4,5,6
6	-	-	-	-	-	-	0-6	0-6
7	-	-	-	-	-	-	-	0-7

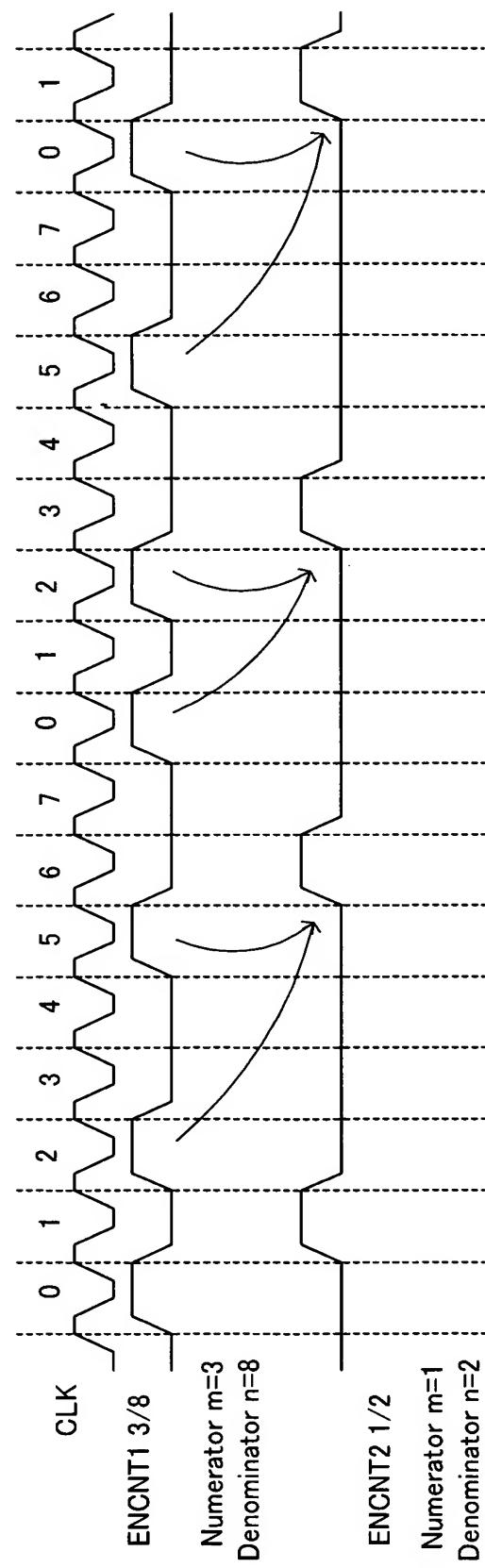
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**FIG. 8**

**Modified Example**



**FIG. 9**



**FIG. 10**

Example of Two-Stage Prescaler

